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Application ID:

10070006

DOUBLE SHIFT INSTRUCTION FOR

MICRO ENGINE USED IN

Title of Invention:

MULTITHREADED PARALLEL

PROCESSOR ARCHITECTURE

First Named Inventor:

Gilbert Wolrich

Domestic/Foreign Application:

Domestic Application

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TRANSMITTAL

Electronic Version v1.1 Stylesheet Version v1.1.0

Title of Invention

DOUBLE SHIFT INSTRUCTION FOR MICRO ENGINE USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE

Application Number:

10/070006

Date:

2002-02-28

First Named Applicant:

Gilbert Wolrich

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Scott C. Harris Registered Number: 32,030	/SCH/	Attorney

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Documents being submitted

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us-ids

10559-302US1_1-usidst.xml

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Comments

IDS # 1 of 4 for U.S. Patent Application Serial No. 10/070,006



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

Title of Invention

DOUBLE SHIFT INSTRUCTION FOR MICRO ENGINE USED IN MULTITHREADED PARALLEL PROCESSOR **ARCHITECTURE**

Application Number:

10/070006

Confirmation Number:

5728

First Named Applicant:

Gilbert Wolrich

Attorney Docket Number: 10559-302US1

Art Unit:

2186

Examiner:

Behzad Peikari

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or 20030191866 or 20040039895 or

20040054880 or 20040071152 or 20040073728

or 20040073778 or 20040098496 or 20040109369 or 20040205747).pn.

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Signature

Examiner Name	Date		